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APPLICATION NO./ CONTROL NO.	FILING DATE	FIRST NAMED INVENTOR / PATENT IN REEXAMINATION	ATTORNEY DOCKET NO.
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EXAMINER
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ART UNIT	PAPER
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20040422


DATE MAILED:

**Please find below and/or attached an Office communication concerning this application or proceeding.**

**Commissioner for Patents**

Applicant's representative Mr. Eric J. Nuss called the examiner on April 21, 2004 notifying that the IDS filed on March 10, 2004 should be considered, because the IDS was originally filed before the notice of allowance mailed on April 16, 2004. Applicant's representative further send copy of the mailing receipt showing that the IDS was received by the office on March 10, 2004. Therefore, the IDS is considered.

Mike Qi  
April 22, 2004

  
**ROBERT H. KIM**  
**SUPERVISORY PATENT EXAMINER**  
**TECHNOLOGY CENTER 2800**

**McKenna Long & Aldridge LLP**  
1900 K Street, NW  
Washington, D.C. 20006  
Voice: (202) 496-7500  
Fax: (202) 496-7756

## FACSIMILE TRANSMITTAL

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**McKenna Long & Aldridge  
LLP**  
1900 K Street, NW  
Washington, D.C. 20006  
Voice: (202) 496-7500  
Fax: (202) 496-7756

**Date:** April 21, 2004

**To:**

Name/Company	Fax No.	Phone No.
Examiner Qi	(571) 273-2299	

**From:** Eric J. Nuss/E. Shavers

**Phone:** (202) 496-7537

**Re:**

**Number of Pages (including cover):** 20

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User No.	579	Client/Matter No.:	8733.046
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Atty Docket No.: 8733.046.00-US

Inventor: Woong S. Choi

Application No.: 09/448,277-Conf. #4859

Filing Date: November 24, 1999

Title: REFLECTION TYPE LIQUID CRYSTAL DISPLAY DEVICE HAVING A HIGH APERATURE RATIO

Documents Filed:

1. IDS (2 pages)
2. PTO/SB/08 with 2 refs.
3. Check No. 113445 \$180.00

Via: PTO Daily Run

Sender's Initials: SKJ/EJN:sbs

Date: March 10, 2004

DC:50265994.1



Docket No.: 8733.046.00-US  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Woong S. Choi

Application No.: 09/448,277

Confirmation No.: 4859

Filed: November 24, 1999

Art Unit: 2871

For: REFLECTION TYPE LIQUID CRYSTAL  
DISPLAY DEVICE HAVING A HIGH  
APERATURE RATIO

Examiner: Z. Q. Qiang

**INFORMATION DISCLOSURE STATEMENT (IDS)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed more than three months after the U.S. filing date, OR more than three months after the date of entry of the national stage of a PCT application, AND after the mailing date of the first Office Action on the merits, whichever occurs first, but before the mailing date of a Final Office Action or Notice of Allowance (37 CFR 1.97(c)).

A summary/abstract translation of the non-English language references is enclosed.

A copy of each reference on the PTO/SB/08 is attached.

DC:50265997.1

Application No.: 09/448,277

2

Docket No.: 8733.046.00-US

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 50-0911, under Order No. 8733.046.00-US. A duplicate copy of this paper is enclosed.

Dated: March 10, 2004

Respectfully submitted,

By 

Eric J. Nuss

Registration No.: 40,106

MCKENNA LONG &amp; ALDRIDGE LLP

1900 K Street, N.W.

Washington, DC 20006

(202) 496-7500

Attorneys for Applicant

DC:50265997.1

Publication number: KR1997-0048828

Publication Date of application: July 29, 1997

Application number: 10-1995-0046250

Filed Date: December 02, 1995

Title: PIXEL ELECTRODE FOR LIQUID CRYSTAL DISPLAY DEVICE

Abstract

The present invention is related to a pixel electrode for use in a liquid crystal display device. In the present invention, the liquid crystal display device includes an upper substrate, a lower substrate, and a liquid crystal layer between the upper and lower substrates. The upper substrate includes a color filter layer, and the lower substrate includes gate lines, data lines, thin film transistors, and pixel electrodes. The pixel electrodes are disposed on an insulator, and a portion of each pixel electrode overlaps a portion of each data lines. Thus, in a normally white mode, a line open inducing a defect of permanent white pixel can be fixed by way of converting into a line short.